



GSV6702

2 In to 1 Out HDMI 2.1 Repeater with
Embedded MCU

April, 2022

Preliminary Product Specification

1. General Description

1.1 General Information

Gschoolink GSV6702 is a high-performance, low-power 2 In to 1 Out HDMI 2.1 repeater. By integrating enhanced microcontroller based on RISC-V, GSV6702 has created a cost-effective solution that provides time-to-market advantages. HDMI Receiver and HDMI Transmitter support up to 48Gbps (FRL, 12G/4Lane). The superior architecture of GSV6702 provides economical smaller footprint solutions using QFN88, targeting applications of Consumer Switch and KVM.

HDCP 1.4 and HDCP 2.2/2.3 are implemented in GSV6702 for its all HDMI ports in HDMI 2.0 and FRL mode. Color Space Conversion is supported at HDMI Tx in HDMI 2.0 mode. Flexible implementations of Audio Insertion, Audio Extraction and SPDIF to I2S conversion features are supported in GSV6702. Embedded CEC and eARC Rx engine enable downstream ARC/eARC to I2S and SPDIF audio extraction features.

An internal Video Generator can be used to generate any uncompressed video timing defined in HDMI 2.1, such as 8K@60Hz, 8K@30Hz, 4K@120Hz, 480i@60Hz.

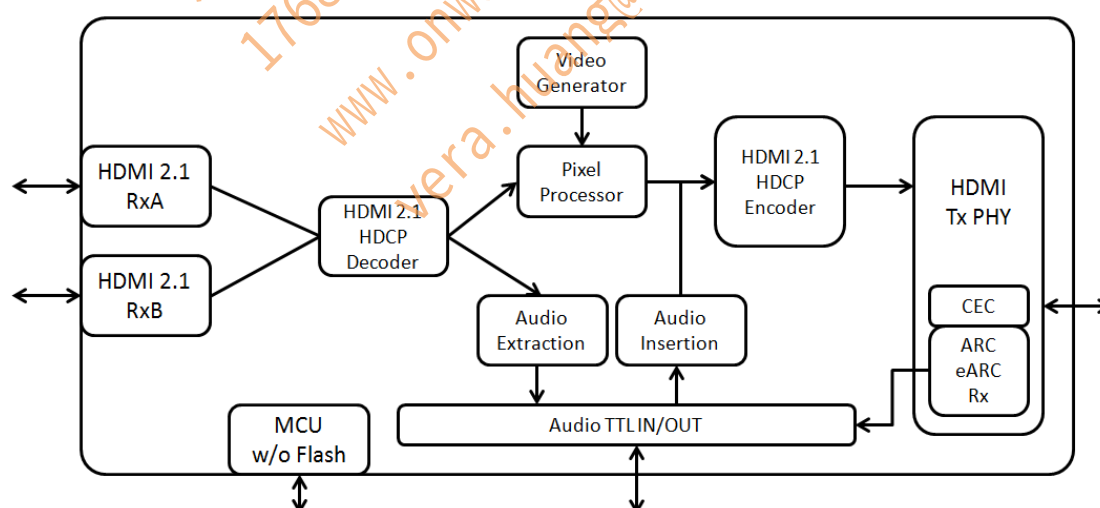


Figure 1. Top Diagram

The supported audio formats are listed in Table 1

Table 1. Supported Audio Format

Packet ID	Packet Type	Sampling Frequency (KHz)		
		32/44.1/48/88.2/ 96/176.4/192	256/352.8/384/ 512/705.6/768	64/128
0x02	Audio Sample Packet (LPCM and Compressed Audio)	Y		Y
0x07	One Bit Audio Sample Packet	Y		
0x08	DST Audio Packet	Y		
0x09	High Bit-rate Audio Stream Packet	Y	Y	

1.2 Features

1.2.1 HDMI Receiver

- Compliant with HDMI 2.1, HDMI 2.0b, HDMI 1.4b
- Compliant with HDCP 2.2/2.3 and HDCP 1.4 in repeater/receiver mode
- Data rate up to 48Gbps (FRL 12Gbps/4 Lane)
- Programmable Adaptive Equalization
- Support High Dynamic Range (HDR) and Dynamic/Static Metadata
- Support Variable Refresh Rate (VRR), FreeSync, G-Sync
- Support ALLM
- Support Forward Error Correction (FEC)
- Support DSC pass-through for compressed input timing
- Embedded arbitrary EDID (up to 512 bytes)
- 5V tolerance on DDC/HPD pins
- 3D format support of frame packing, side-by-side, top-and-bottom

1.2.2 HDMI Transmitter

- Compliant with HDMI 2.1a, HDMI 2.0b, HDMI 1.4b
- Compliant with HDCP 2.2/2.3 and HDCP 1.4
- Data rate up to 48Gbps (FRL 12Gbps/4 Lane)
- Programmable Voltage Swing, Slew-Rate and Pre-emphasis
- Support AC-coupling on TMDS input/output
- Support Color Space Converter in TMDS mode
- Support HDR (HDR10/HDR10+/Dolby Vision/HLG)
- Support Variable Refresh Rate (VRR), FreeSync, G-Sync
- Support ALLM

- Support DSC encoded stream pass-through from HDMI/DP input
- Hardware CEC Engine for Low Level protocol decoding
- 5V tolerance on DDC/HPD/CEC pins
- 3D format support of frame packing, side-by-side, top-and-bottom
- Support eARC Rx to I2S/SPDIF audio extraction in HDMI 2.1a

1.2.3 Audio Input/Output

- I2S and SPDIF Audio Extraction from HDMI Rx
- I2S and SPDIF Audio Extraction from downstream ARC /eARC
- I2S/SPDIF Audio Insertion to HDMI Tx
- SPDIF/I2S/HBR/DSD/TDM Format Supported for Audio Extraction and Insertion
- SPDIF to I2S Conversion using single Bi-directional TTL bus

1.2.4 System Features

- Optional External MCU (via I2C)/ Internal MCU mode
- Embedded MCU and External Flash
- External pins of Flash QSPI interface
- External 25MHz Crystal required
- Available Pins for UART/Timer/GPIO control from embedded MCU
- Mailbox feature for external MCU access on chip function status
- Temperature Sensor Monitoring Circuit

1.3 Chip Application Modes

1.3.1 HDMI switch with audio extraction

RxA/RxB are configured as HDMI Rx Inputs. This mode is applicable for monitor application. GSV6702 can extract audio from HDMI Rx to Audio TTL Pins, while loop out Video/Audio to HDMI Tx. Video can be manipulated and audio can also be muted/unmuted. An dedicated AVMUTE pin is used to indicate current AVMUTE status change interrupt.

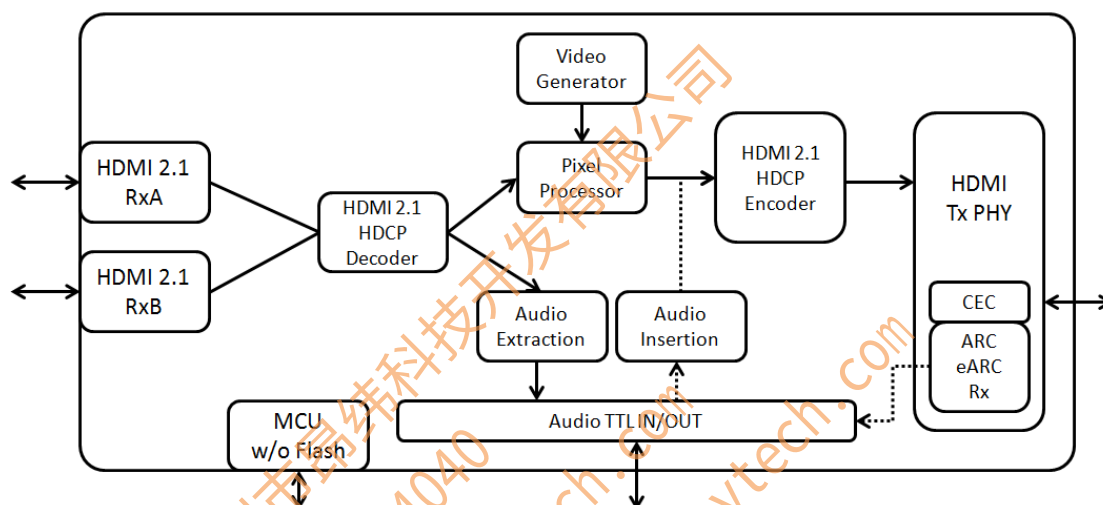


Figure 2. Input Switch Between Different Inputs

1.3.2 HDMI switch with audio insertion

I2S/SPDIF audio stream can be inserted into HDMI Tx if required.

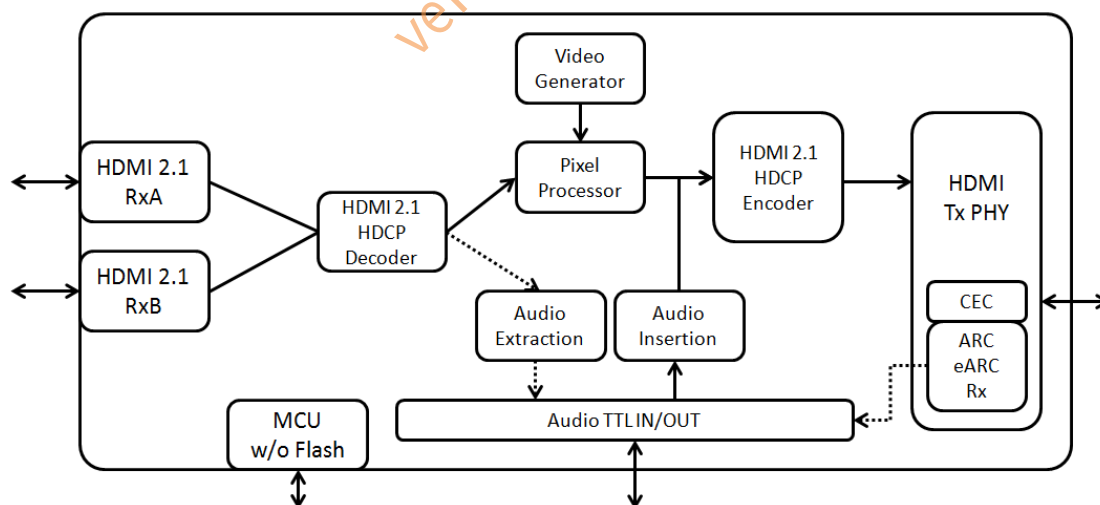


Figure 3. Audio Insertion Application

1.3.3 Audio Return to Audio Extraction

With embedded CEC engine, GSV6702 HDMI Tx can support ARC return channel defined in HDMI 1.4b specification. GSV6702 can convert SPDIF in ARC return stream to both SPDIF and I2S TTL output on Audio Bus.

With embedded eARC Rx engine, GSV6702 HDMI Tx can support eARC return channel defined in HDMI 2.1a specification. GSV6702 can convert SPDIF in eARC return stream to both SPDIF and I2S TTL output on Audio Bus.

HDMI Tx differential pairs can output HDMI 2.1 to downstream while ARC/eARC feature is enabled.

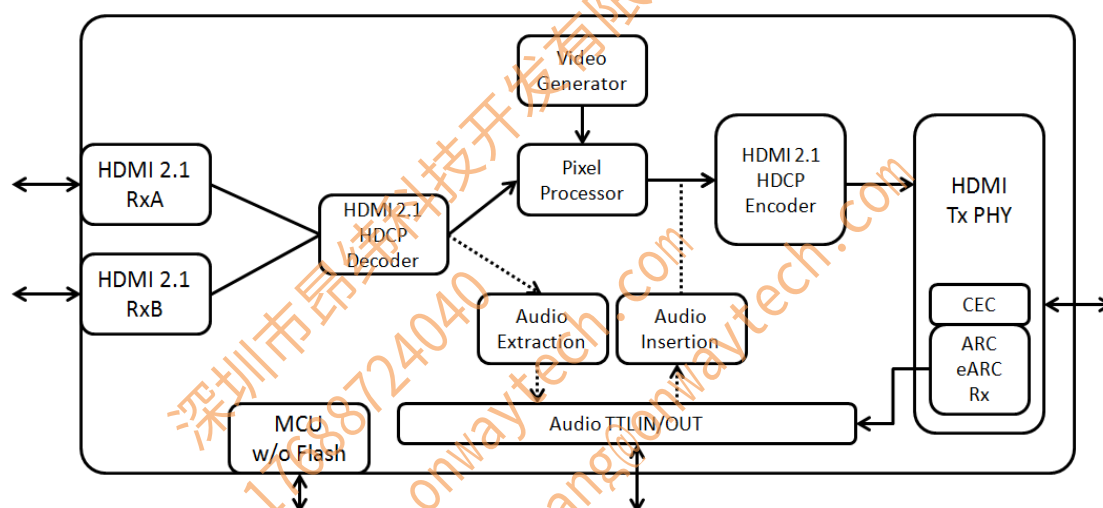


Figure 4. Audio Return Application

1.4 Audio Bus Output Configuration

When one group of audio bus is configured as output, I2S and SPDIF are output at the same time. General configuration of pin settings is shown below:

Table 2. I2S/SPDIF Audio Extraction

Pin Name	Alias	Direction	Description
AUD_D0	SDATA[0]	Output	I2S Data, default stereo channels
AUD_D1	SDATA[1]	Output	I2S Data, 3/4 channels
AUD_D2	SDATA[2]	Output	I2S Data, 5/6 channels
AUD_D3	SDATA[3]	Output	I2S Data, 7/8 channels

2. Pin Description

2.1 Pin Diagram

QFN88 Pin definition is defined as below.

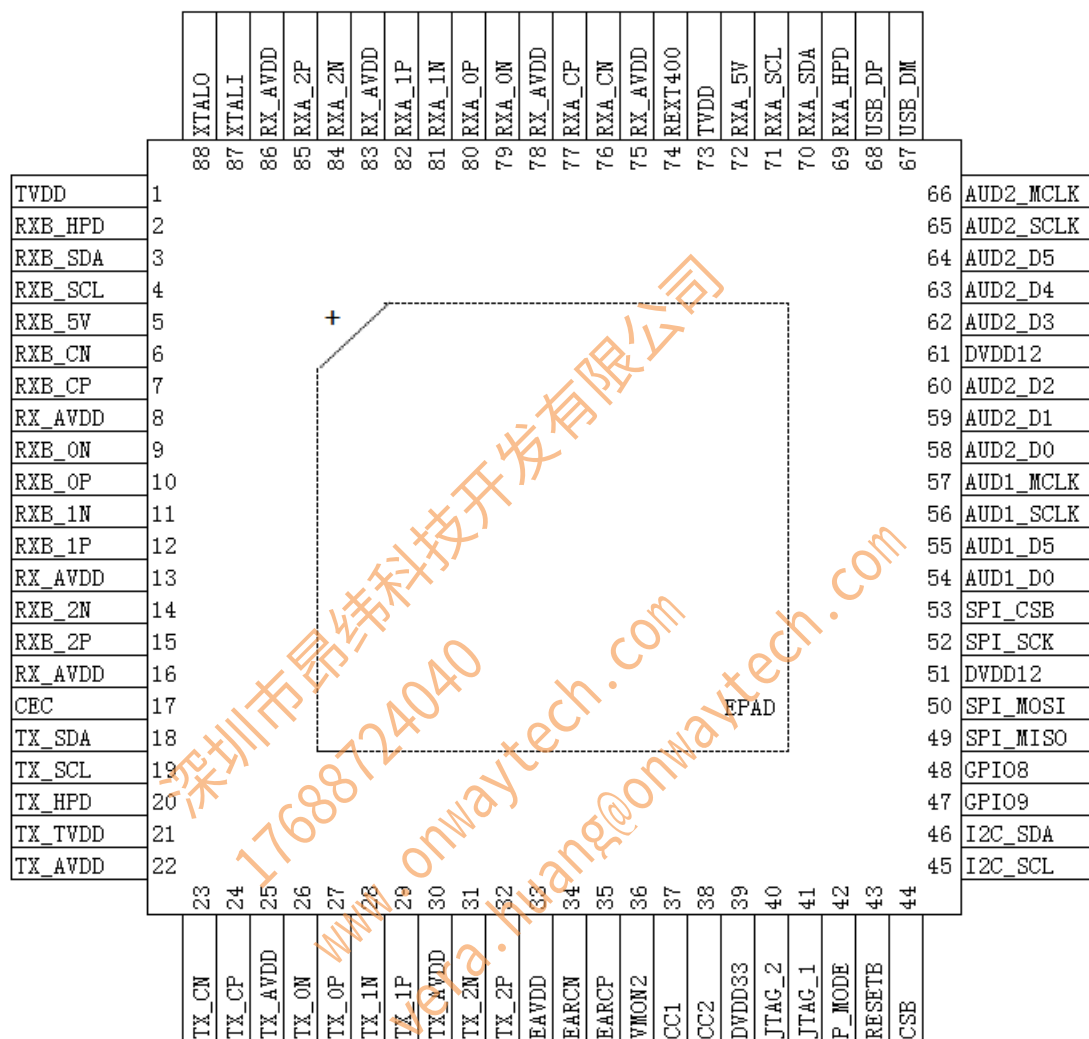


Figure 7. GSV6702 QFN88 Pin Diagram

2.2 QFN88 Pin Description

Table 8. QFN88 Pin Description

Pin Name	Direction	Pin No.	Description
HDMI RxA Pins			
RXA_5V	I	72	HDMI: RXA 5V Detection PAD
RXA_HPDP	I/O	69	HDMI: RXA HPD PAD
RXA_SDA	I/O	70	HDMI: RXA DDC SDA PAD

4. Package Information

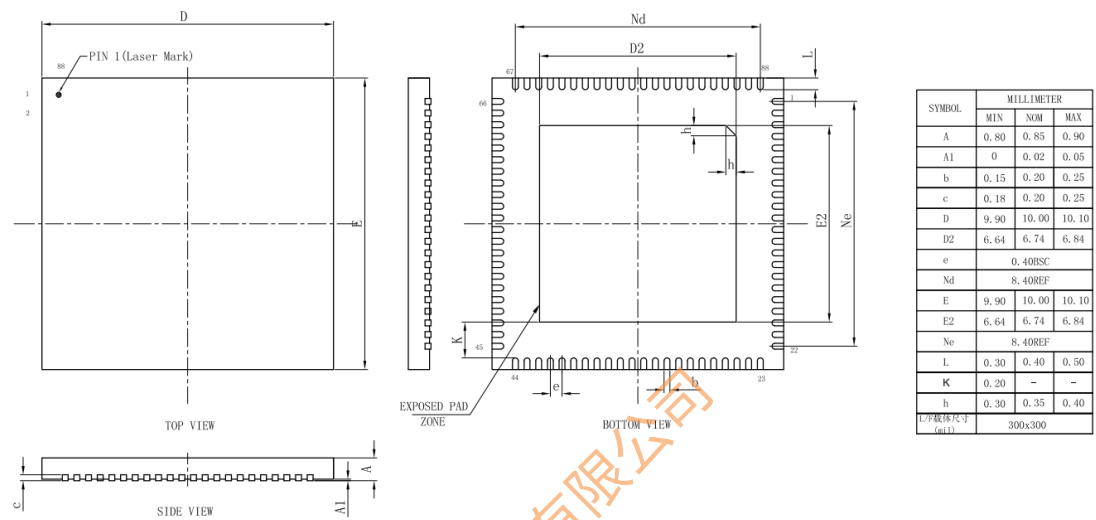


Figure 14. Package Dimensions (QFN88)

5. Ordering Guide

Table 12. Ordering Information

Part Number.	Temperature Range	Package Description	Packing Type
GSV6702	−20°C to +85°C	QFN88	Tray

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